

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	45	(phase adj shift\$4) and ((load adj variat\$4) (isolattor adj free)) and amplifier and "330"/\$.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 10:55
L2	11	(phase adj shift\$4) and ((load adj variat\$4) (isolattor adj free)) and 330/107,278-279,149,151.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 10:59
L3	11	(phase adj shift\$4) and ((load adj variat\$4) (isolattor adj free)) and 330/107,278-279,149,151.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 10:59
L4	1121	330/278.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 11:00
L10	1	(phase adj shifter) and (detector with (forward adj signal)).clm.	US-PGPUB	OR	ON	2008/03/27 11:10
L11	416	VSWR and (phase adj shift\$4) and gain and control\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 11:10
L12	216	reflected and ((phase adj (shift \$4 adjust\$4)) with between with stage)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 11:11

L13	800	330/278-278,149. ccls. and (phase adj shift\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 11:13
L14	164	330/278-278,149. ccls. and (phase adj shift\$4) and (control adj circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 11:13
L15	203	330/278-278,149. ccls. and (phase adj shift\$4) and (control adj (circuit unit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 11:13
L16	2	330/278-278,149. ccls. and (phase adj shift\$4) and (control adj (circuit unit)) and (forward same reflected)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/27 11:13

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